

The diagram illustrates the Apollo Core Rope ROM, a 64 Words x 16-Bit memory device. It is a Woven Machine Code ROM, VEB Elektronik Gera 5221.3 Cores, with a 1.024 Bits capacity.

The diagram shows the internal structure of the ROM, including the SENSE IN and SENSE OUT lines, and the DIP-40 ZIF INTERFACE (600 MIL).

The diagram is divided into two main sections: the top section shows the internal structure of the ROM, and the bottom section shows the DIP-40 ZIF INTERFACE (600 MIL).

The top section shows the internal structure of the ROM, which is a 64 Words x 16-Bit memory device. It is a Woven Machine Code ROM, VEB Elektronik Gera 5221.3 Cores, with a 1.024 Bits capacity. The diagram shows the internal structure of the ROM, including the SENSE IN and SENSE OUT lines, and the DIP-40 ZIF INTERFACE (600 MIL).

The bottom section shows the DIP-40 ZIF INTERFACE (600 MIL). The diagram shows the internal structure of the ROM, including the SENSE IN and SENSE OUT lines, and the DIP-40 ZIF INTERFACE (600 MIL).

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The diagram shows the internal structure of the ROM, including the SENSE IN and SENSE OUT lines, and the DIP-40 ZIF INTERFACE (600 MIL).

The SENSE IN lines are labeled D0 through D15, and the SENSE OUT lines are labeled 01 through 015. The diagram shows the internal structure of the ROM, including the SENSE IN and SENSE OUT lines, and the DIP-40 ZIF INTERFACE (600 MIL).

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The SENSE IN and SENSE OUT lines are connected to the ROM. The SENSE IN lines are labeled D0 through D15, and the SENSE OUT lines are labeled 01 through 015. The ROM is connected to a DIP-40 ZIF INTERFACE (600 MIL).

The diagram also shows the internal structure of the ROM, including the SENSE IN and SENSE OUT lines, and the DIP-40 ZIF INTERFACE (600 MIL).

The diagram illustrates the Apollo Core Rope ROM, a 64 Words x 16-Bit memory device. It is composed of two 32-bit halves, each with 64 words. The top half is labeled "SENSE IN" and the bottom half is labeled "SENSE OUT". The diagram shows the internal structure of the ROM, including the 64 words and the 16-bit data bus. The ROM is connected to a DIP-40 ZIF interface (600 MIL) via a 40-pin connector. The diagram also shows the physical layout of the ROM, including the 40 pins and the 64 words.

Woven Machine Code • VEB Elektronik Gera 5221.3 Cores • 1.024 Bits

SENSE IN

0 4 8 12 16 20 24 28

00 01
02 03
04 05
06 07
08 09
D10 D11
D12 D13
D14 D15

SENSE OUT

01 02
03 04
05 06
07 08
09 10
11 12
13 14
15 16

32 36 40 44 48 52 56 60

Relay Computer Project • Apollo-11 AGC Architecture Replica

DIP-40 ZIF INTERFACE (600 MIL)

