

The diagram illustrates the Apollo Core Rope ROM, a 64 Words x 16-Bit memory device. It is a Woven Machine Code ROM, VEB Elektronik Gera 5221.3 Cores, with a 1.024 Bits capacity.

The diagram shows the internal structure of the ROM, including the SENSE IN and SENSE OUT lines, and the DIP-40 ZIF INTERFACE (600 MIL).

The diagram shows the internal structure of the ROM, including the SENSE IN and SENSE OUT lines, and the DIP-40 ZIF INTERFACE (600 MIL).

The diagram illustrates the Apollo Core Rope ROM, a 64 Words x 16-Bit memory device. It is a Woven Machine Code ROM, VEB Elektronik Gera 5221.3 Cores, with a 1.024 Bits capacity.

The diagram shows the internal structure of the ROM, including the SENSE IN and SENSE OUT lines, and the DIP-40 ZIF INTERFACE (600 MIL).

The diagram shows the internal structure of the ROM, including the SENSE IN and SENSE OUT lines, and the DIP-40 ZIF INTERFACE (600 MIL).

The diagram illustrates the Apollo Core Rope ROM, a 64 Words x 16-Bit memory device. It is a Woven Machine Code ROM, VEB Elektronik Gera 5221.3 Cores, with a 1.024 Bits capacity.

The diagram shows the internal structure of the ROM, including the SENSE IN and SENSE OUT lines, and the DIP-40 ZIF INTERFACE (600 MIL).

The diagram shows the internal structure of the ROM, including the SENSE IN and SENSE OUT lines, and the DIP-40 ZIF INTERFACE (600 MIL).

The diagram illustrates the Apollo Core Rope ROM, a 64 Words x 16-Bit memory device. It is a Woven Machine Code ROM, VEB Elektronik Gera 5221.3 Cores, with a 1.024 Bits capacity.

The diagram shows the internal structure of the ROM, including the SENSE IN and SENSE OUT lines, and the DIP-40 ZIF INTERFACE (600 MIL).

The diagram shows the internal structure of the ROM, including the SENSE IN and SENSE OUT lines, and the DIP-40 ZIF INTERFACE (600 MIL).

The diagram illustrates the Apollo Core Rope ROM, a 64 Words x 16-Bit memory device. It is a Woven Machine Code ROM, VEB Elektronik Gera 5221.3 Cores, with a 1.024 Bits capacity.

The diagram shows the internal structure of the ROM, including the SENSE IN and SENSE OUT lines, and the DIP-40 ZIF INTERFACE (600 MIL).

The diagram shows the internal structure of the ROM, including the SENSE IN and SENSE OUT lines, and the DIP-40 ZIF INTERFACE (600 MIL).

